Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20050097497".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 15:36
L2	3	spatia\$4 same plac\$5 same subsystem same tile	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 15:57
L3	2	l1 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 15:37
L4	58122	plac\$5 same (subsystem or circuit)same (tile or substrate or layout)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:18
L5	122035	(rout\$3 or interconnect\$4)same (tile or substrate or layout)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 16:04
L6	603	virtual adj5 wire	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 16:04
L7	2	switch same register and I1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 16:11
L8	1	(two adj3 register) same latch\$3 and l1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:19

L9	1	(repeat\$4 or loop\$3)same register same latch\$3 and l1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:01
L10	2461	(two adj3 register) same latch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:26
L11	63141	((two adj3 register)or registers) same latch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:55
L12	5362	schedul\$4 near9 cycle	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:36
L13	120887	repeat\$4 near9 cycle	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:30
L15	2	I1 and virtual and (subsystem or sub-system)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:05
L16	8178	(schedul\$4 or manag\$4) near9 cycle	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:39
L17	1		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:39
L18	10903	(schedul\$4 or manag\$5) near9 cycle	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:54

L19	1	I4 and I11 and I13 and I18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:54
L20	25190	(count\$4 or manag\$5) near9 loop\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 17:54
L21	3	l4 and l11 and l13 and l20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:00
L22	52	l4 and l11 and l20,	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:20
L23	36	cycl\$3 and virtual and I22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:21
L24	36	123 and clock and period	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:10
L25	1	map\$4 near9 (subsystem or sub-system)and I24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:07
L26	36	map\$4 and 124	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:08
L27	2	map\$4 same virtual and I24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:08
L28	193355	plac\$5 near9((block near20 circuit)subsystem or circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:19

L29	264229	((block near20 circuit)subsystem or circuit) near9 (tile or substrate or layout)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:20
L30	224		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:20
L31	10466	cycl\$3 and virtual and clock and period	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:21
L32	4 5	I30 and I31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:25
L33	65	Michael near5 Schlansker and HEWLETT	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:26
L34	4	I33 and I20 and virtual	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 18:27